

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>1</b>	<b>L1</b>	<b>2627</b>	<b>(361/760,777,778,782,805).ccls.</b>	<b>US- PGPUB ; USPAT</b>
<b>2</b>	<b>L2</b>	<b>1781</b>	<b>174/261.ccls.</b>	<b>US- PGPUB ; USPAT</b>
<b>3</b>	<b>L3</b>	<b>1501</b>	<b>(307/38,106).ccls.</b>	<b>US- PGPUB ; USPAT</b>
<b>4</b>	<b>L4</b>	<b>975</b>	<b>(333/130,156,159,161,164).ccls.</b>	<b>US- PGPUB ; USPAT</b>
<b>5</b>	<b>L5</b>	<b>504</b>	<b>710/306.ccls.</b>	<b>US- PGPUB ; USPAT</b>
<b>6</b>	<b>L6</b>	<b>6906</b>	<b>1 2 3 4 5</b>	<b>US- PGPUB ; USPAT</b>
<b>7</b>	<b>L7</b>	<b>143</b>	<b>6 and @pd&gt;="20050220"</b>	<b>US- PGPUB ; USPAT</b>
<b>8</b>	<b>L8</b>	<b>31</b>	<b>7 and resistor\$1</b>	<b>US- PGPUB ; USPAT</b>

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>9</b>	<b>L9</b>	<b>112</b>	<b>7 not 8</b>	<b>US- PGPUB ; USPAT</b>
<b>10</b>	<b>L10</b>	<b>66</b>	<b>9 and (control controller\$1 pll (phase\$1 adj lock\$2 adj loop))</b>	<b>US- PGPUB ; USPAT</b>
<b>11</b>	<b>L11</b>	<b>46</b>	<b>9 not 10</b>	<b>US- PGPUB ; USPAT</b>
<b>12</b>	<b>L12</b>	<b>5</b>	<b>11 and (bridg\$2 spanning)</b>	<b>US- PGPUB ; USPAT</b>
<b>13</b>	<b>L13</b>	<b>41</b>	<b>11 not 12</b>	<b>US- PGPUB ; USPAT</b>